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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\* *9/22*

This application is a DIV of 09/776,681 02/06/2001 PAT 6,635,934

\*\* FOREIGN APPLICATIONS \*\*\*\* *9/22*

JAPAN 2000-167189(P) 06/05/2000

JAPAN 2000-261703(P) 08/30/2000

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 54	TOTAL CLAIMS 4	INDEPENDENT CLAIMS 1
Verified and Acknowledged Examiner's Signature <i>J. Schmitz</i>	Initials				

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## TITLE

Semiconductor integrated circuit device operating with low power consumption

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